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| --- | --- | --- |
| Instruction set (Reduced [RISC]/ complex [CISC]) |  |  |
| Manufacturers |  | ARM (Snapdragon, Qualcomm etc under license) |
| Most common use / product | PCs / servers |  |
| Estimated number of devices |  |  |
| Power consumption (3W/45W) |  |  |
| Manufacturing size (7nm/10nm) |  |  |
| Cheaper (✓🗶) |  |  |
| Often has more cores (✓🗶) |  |  |
| Better at pipelining / concurrency |  |  |
| More load on RAM / storage (✓🗶) |  |  |
| Architecture (Von Neumann / Harvard) |  |  |